

FIG. 1

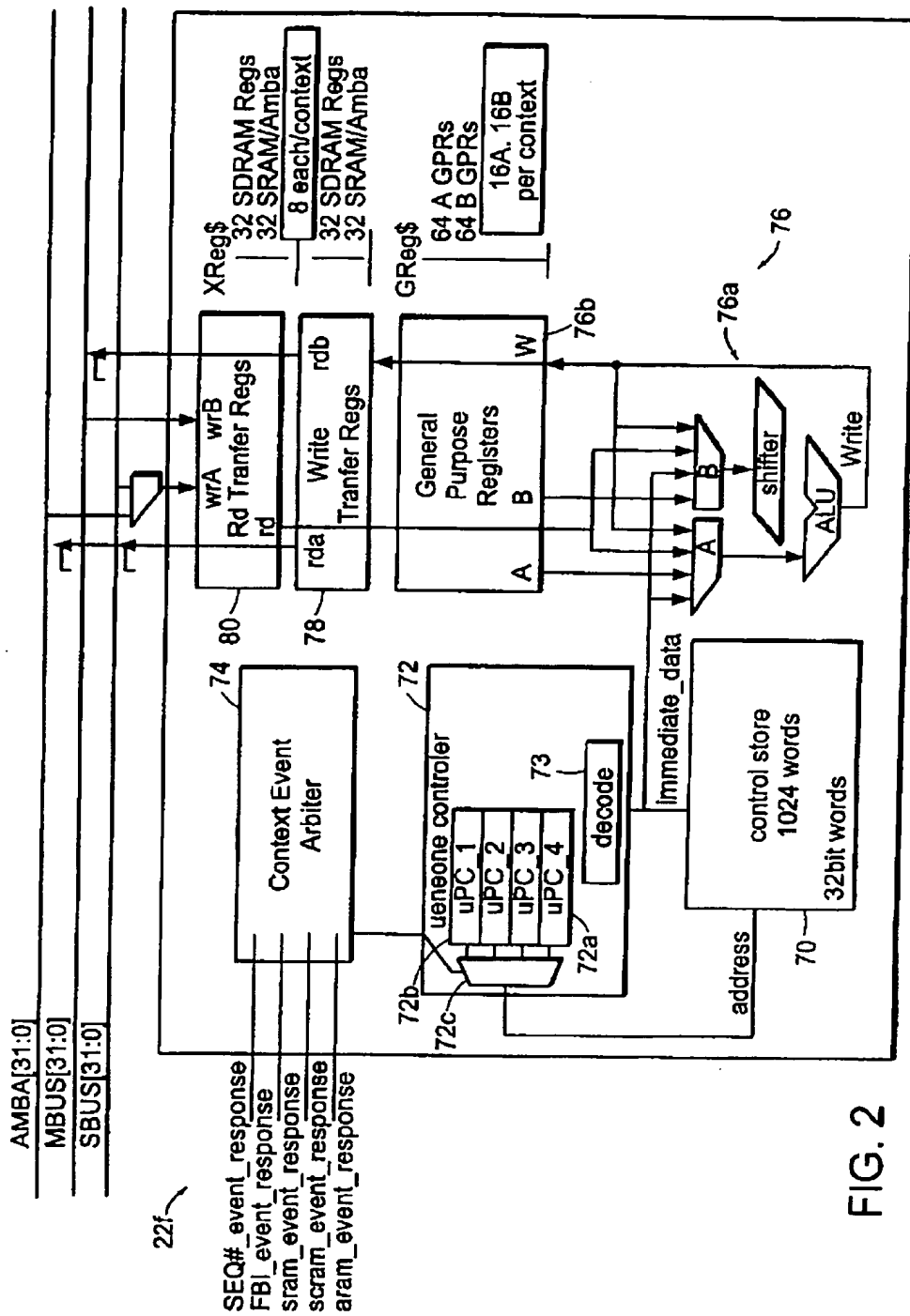


FIG. 2

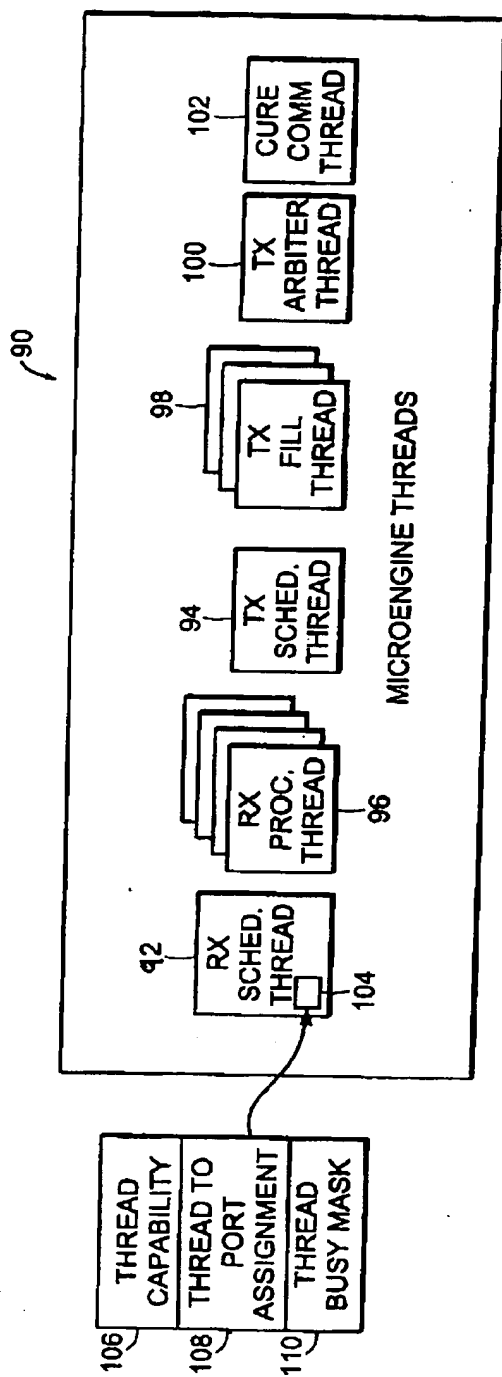


FIG. 3

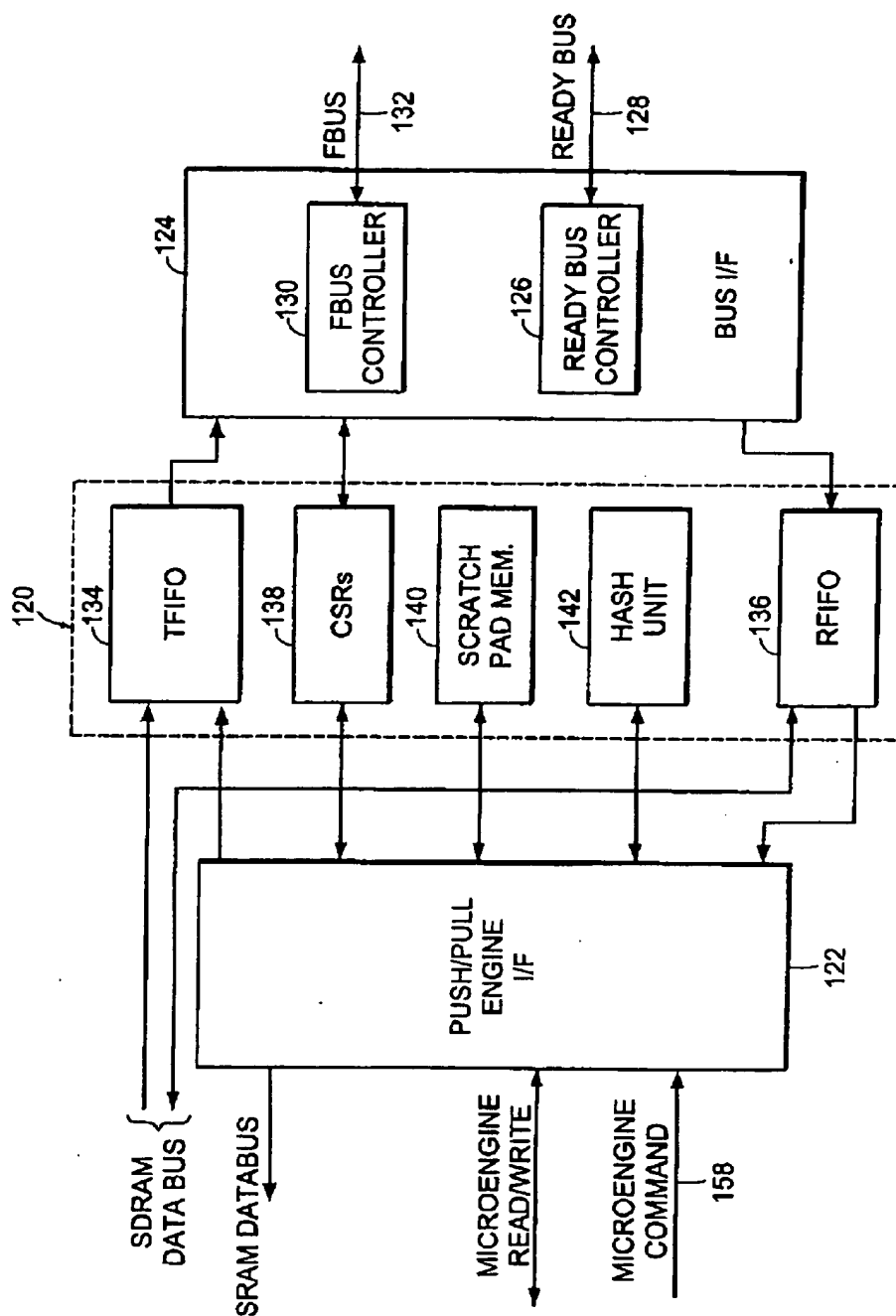


FIG. 4

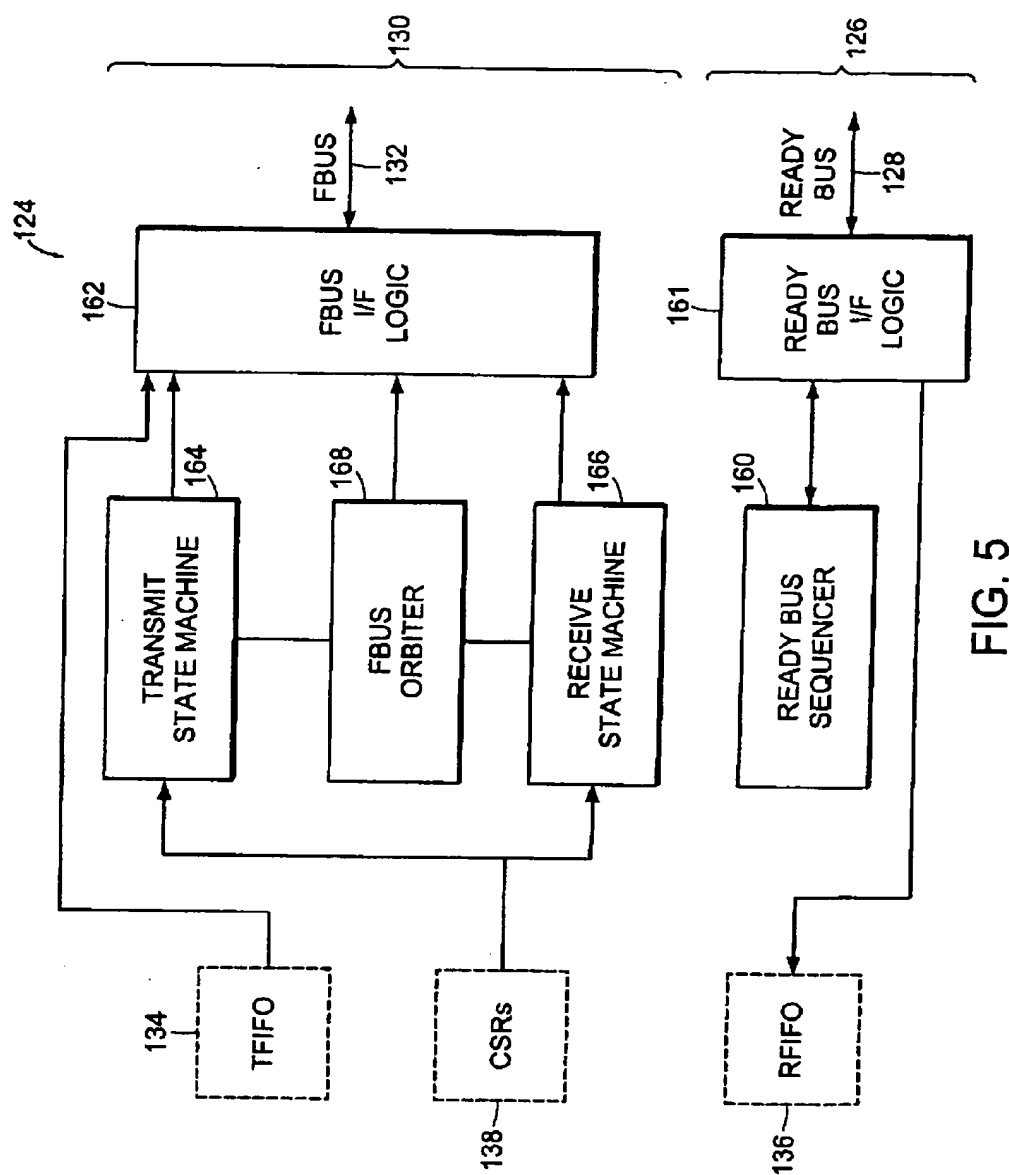


FIG. 5

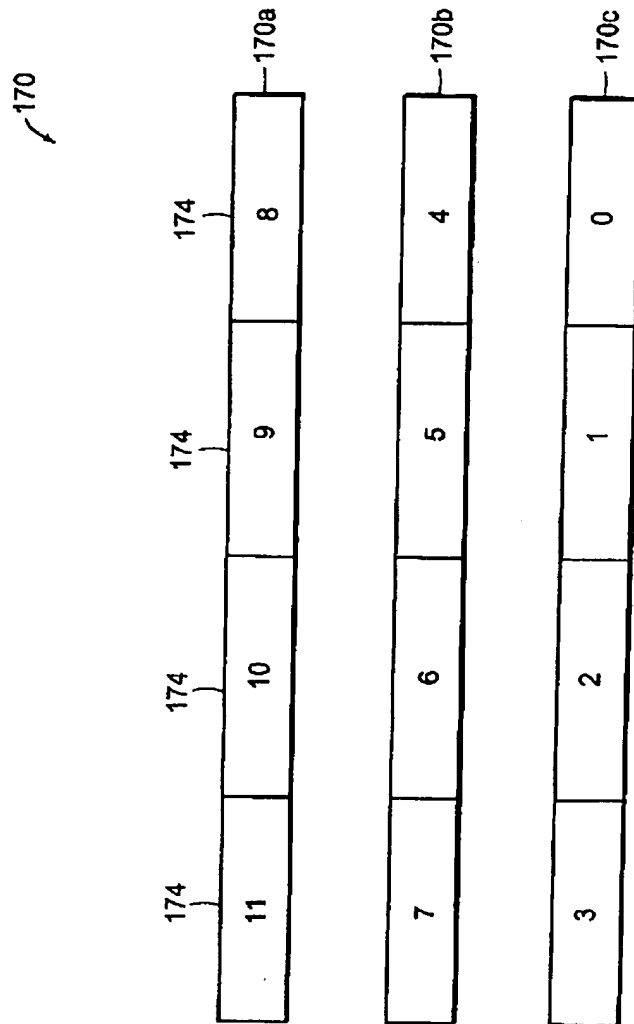


FIG. 6A

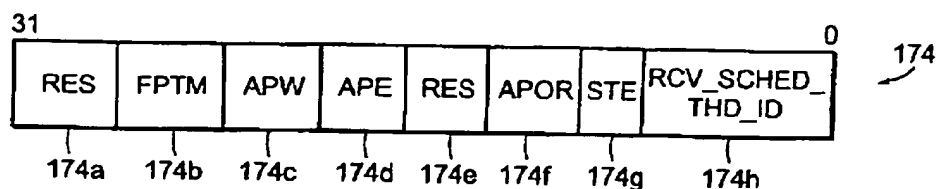


FIG. 6B

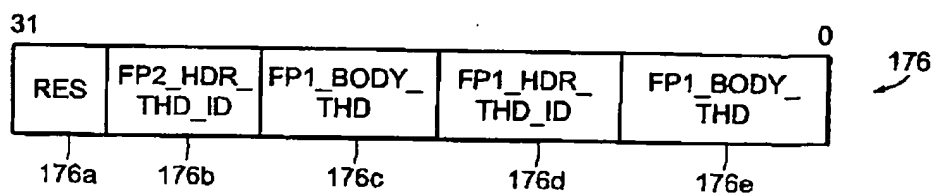


FIG. 6C



FIG. 6D

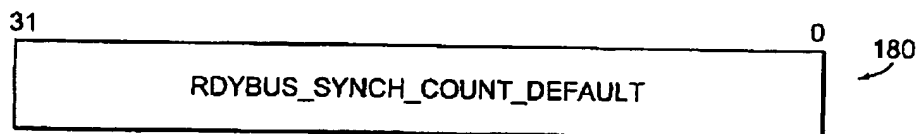


FIG. 6E

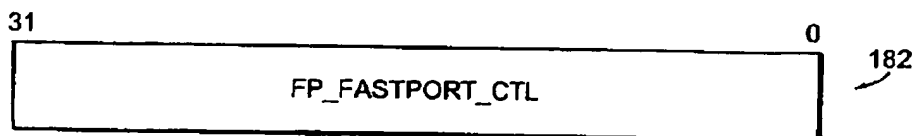


FIG. 6F

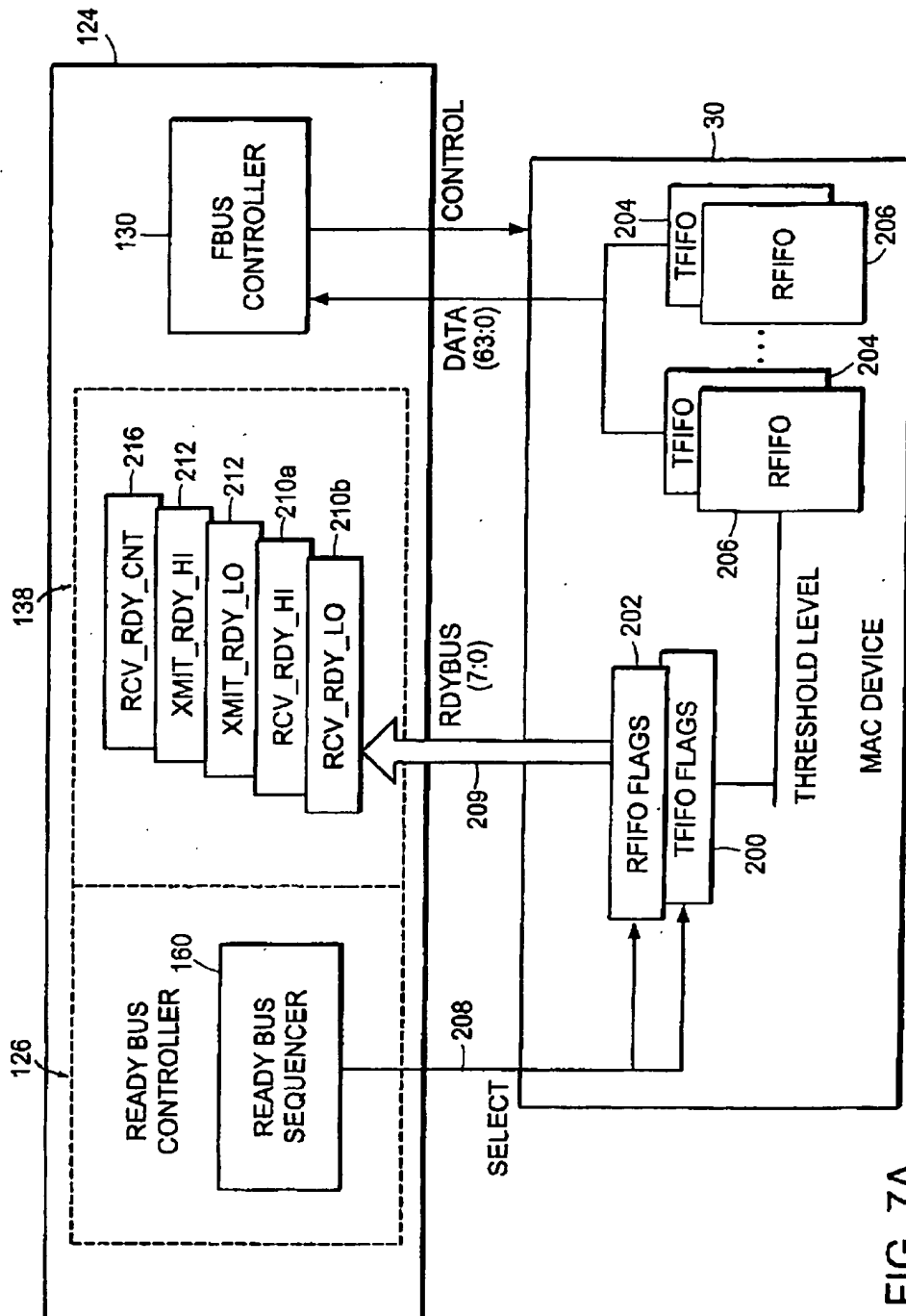


FIG. 7A

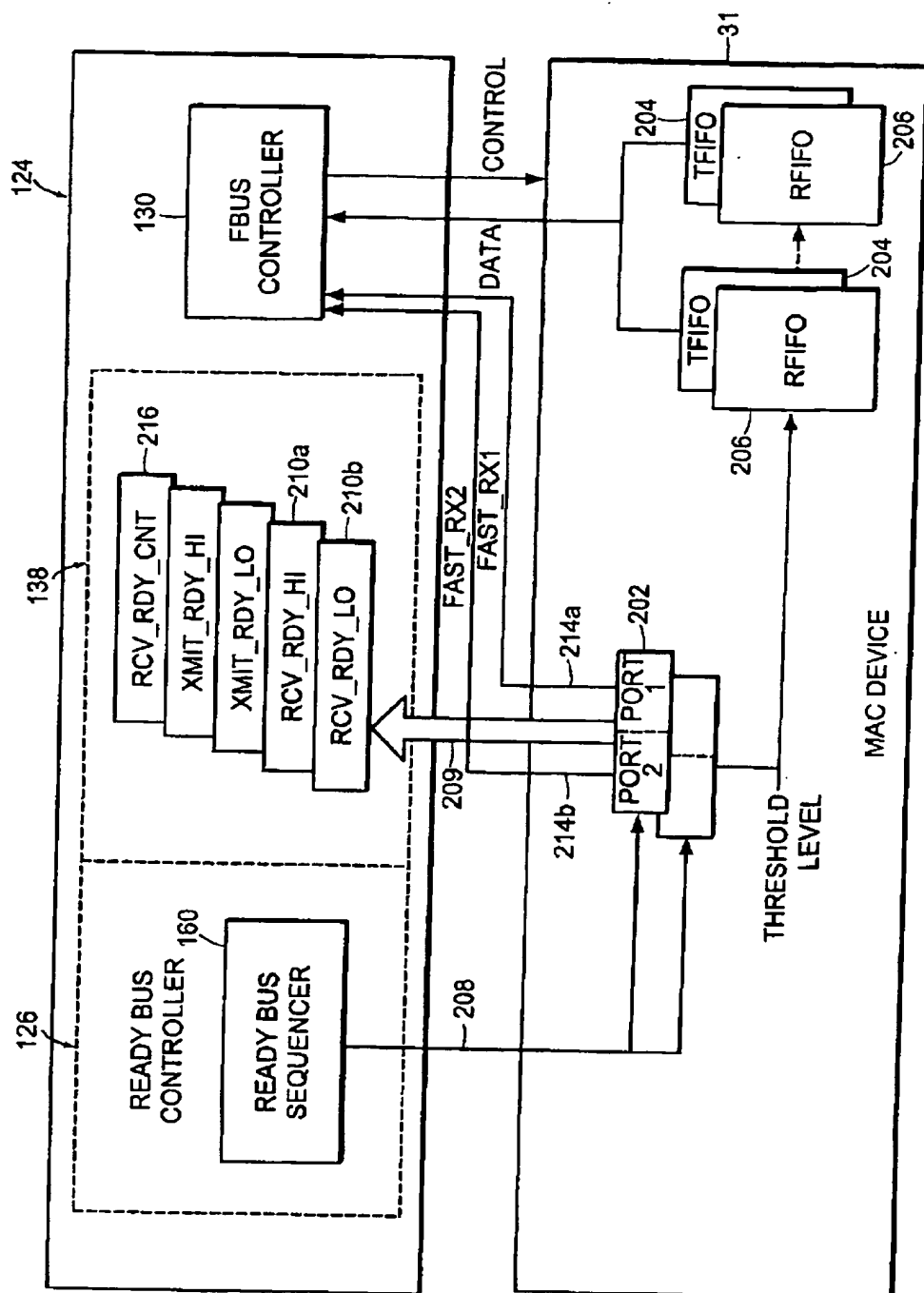


FIG. 7B

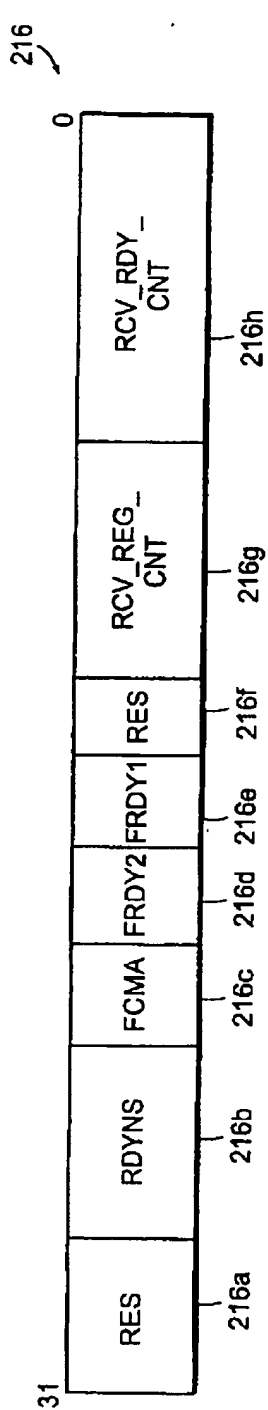


FIG. 8A

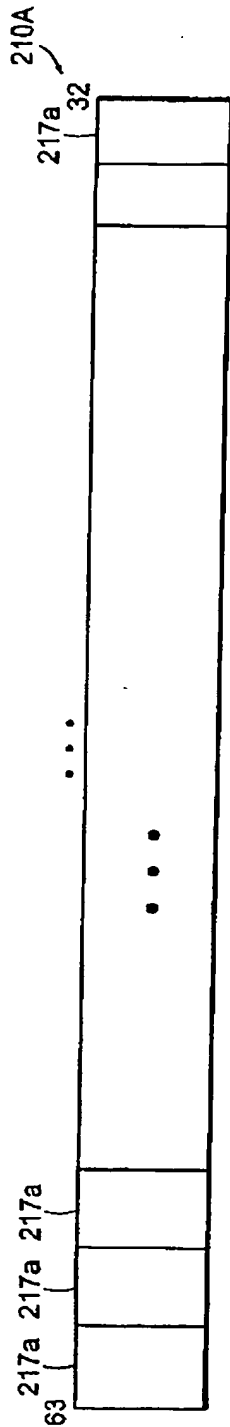


FIG. 8B

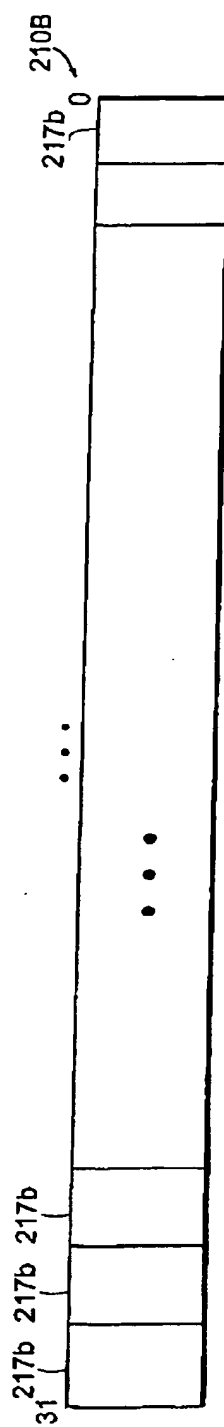


FIG. 8C

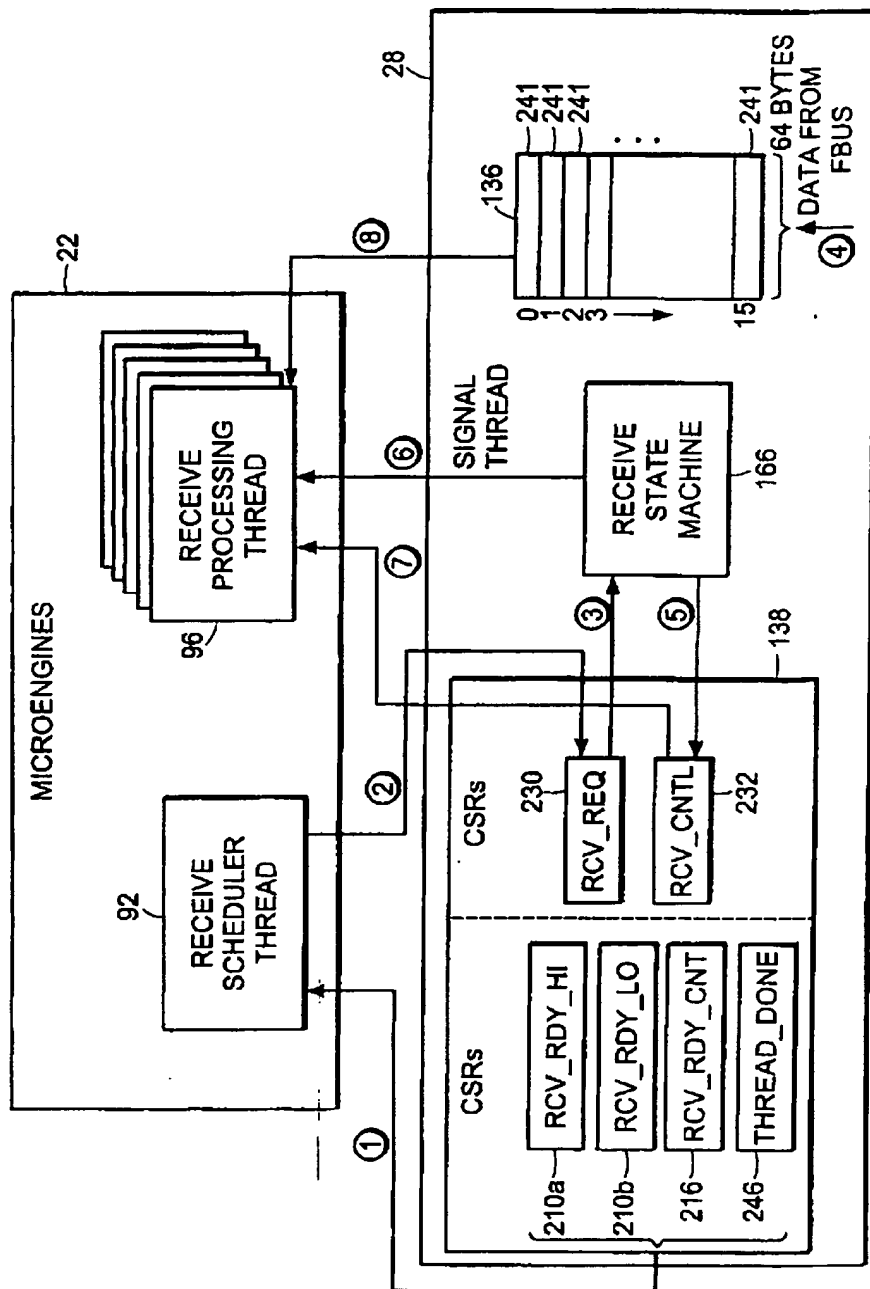


FIG. 9

230

230a	230b	230c	230d	230e	230f	230g	230h	230i	230j	230k	230l	230m	
RES	FA		JL	E2	E1	FS	NFE	IGFR	STGRS	TID	RM	RP	0
													1

231

231

FIG. 10A

232

232a	232b	232c	232d	232e	232f	232g	232h	232i	232j	232k	232l	
THRSG	MACPORT/ THD	SOP SEQ#	RF	REAR	SE	FE	EFOR	SNOR	VALID BYTES	EOP	SOP	0
												1
												2
												3

233

233

233

233

FIG. 10B

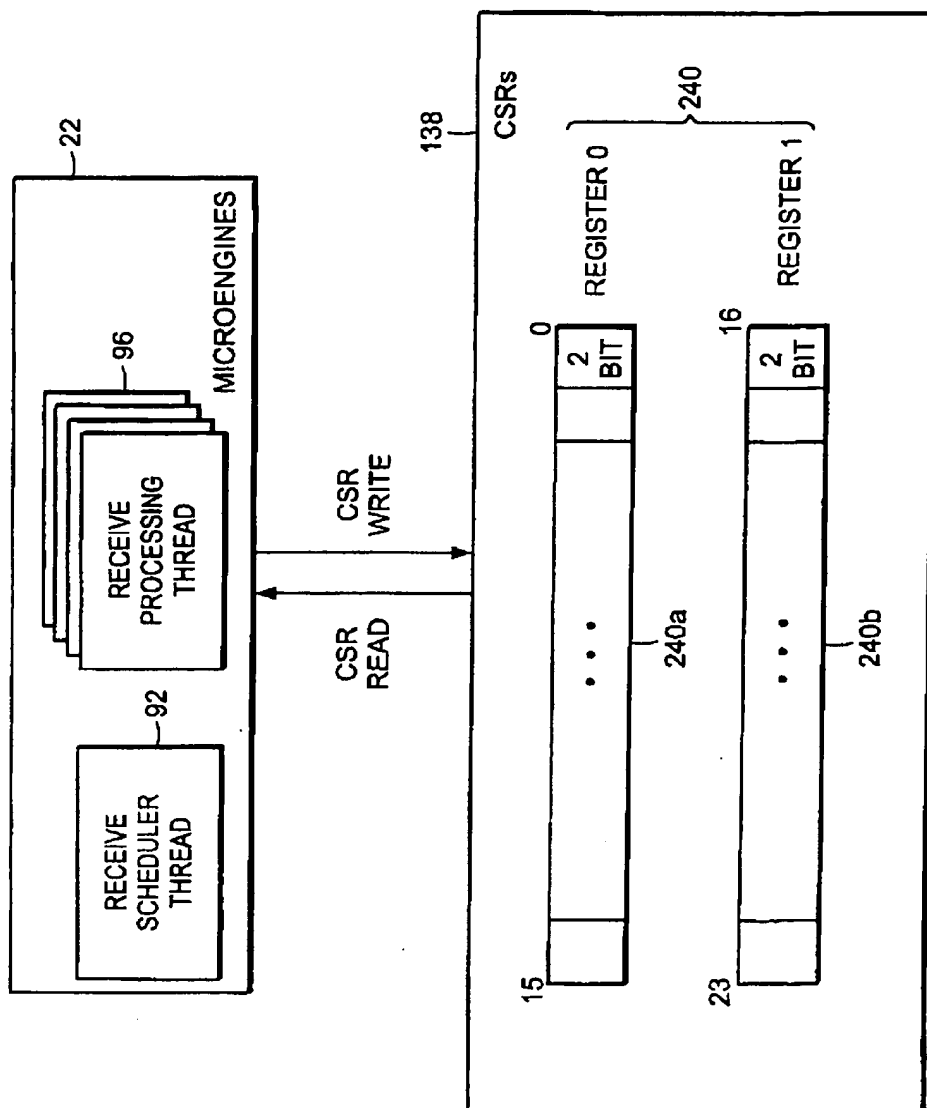


FIG. 11